

# SHARED LOGIC FOR DECODING AND DEINTERLACING OF COMPRESSED VIDEO

## Abstract

5           One embodiment includes a method that includes receiving a compressed  
video stream. The method also includes decoding a number of blocks of the  
compressed video stream to output a number of blocks of decoded video data. The  
decoding is based on at least one motion compensation vector. The method also  
includes deinterlacing the number of blocks of the decoded video data to output  
10 deinterlaced video data. The deinterlacing of one of the blocks of the number of  
blocks is based on the at least one motion compensation vector if a prediction error  
energy for the at least one motion compensation vector for the block is less than a  
threshold.

"Express Mail" mailing label number: EV415949645US

Date of Deposit: March 31, 2004

This paper or fee is being deposited on the date indicated above with  
the United States Postal Service pursuant to 37 CFR 1.10, and is  
addressed to the Commissioner for Patents, Mail Stop Patent  
Application, P.O. Box 1450, Alexandria, VA 22313-1450.